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Complete if Known

Application Number	10/728451
Filing Date	December 5, 2003
First Named Inventor	Scheuerlein, Roy
Group Art Unit	Unknown 2815
Examiner Name	Unknown Richards

Sheet 1 of 1

Attorney Docket No: MA-112

US PATENT DOCUMENTS

Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
WJL	A1	10/935078		Scheuerlein et al.			12/31/2002
WJL	A2	10/671248		Cleaves			06/30/2003
WJL	A3	5835396	11/10/1998	Zhang			
WJL	A4	6034882	03/07/2000	Johnson et al.			
WJL	A5	6185121	02/06/2001	O'Neill			
WJL	A6	US20020079553A1	06/27/2002	Cleaves			

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WJL	A7	SCHEUERLEIN, ROY.E., et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", 2000 IEEE International Solid-State Circuits Conference: ISSCC 2000, Feb 8, 2000: Emerging Memory and Device Technologies/Paper TA 7.2, (2/8/2000), 128-129	

EXAMINER

DATE CONSIDERED

9/24/05

Substitute Disclosure Statement Form (PTO-1449)

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